. 4.2



FIG. 1

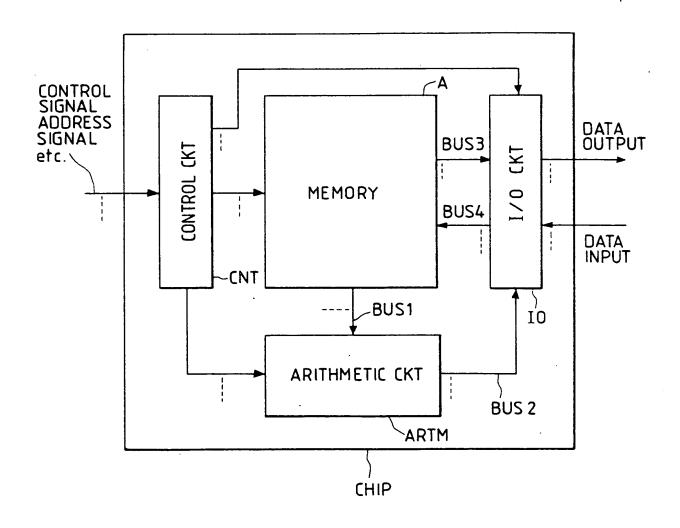


FIG. 2(a)

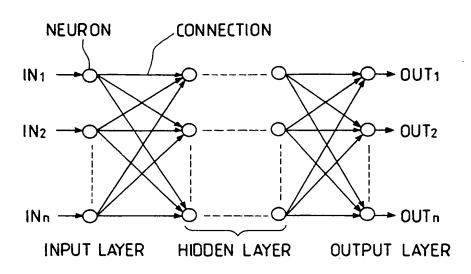


FIG. 2(b)

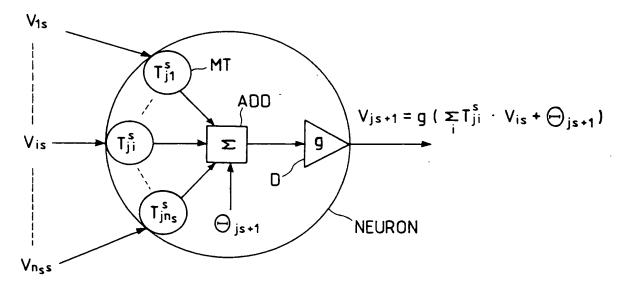


FIG. 2(c)

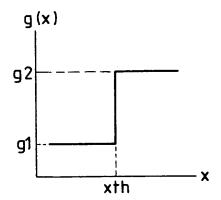


FIG. 2(d)

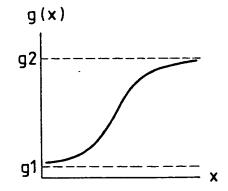


FIG. 3(a)

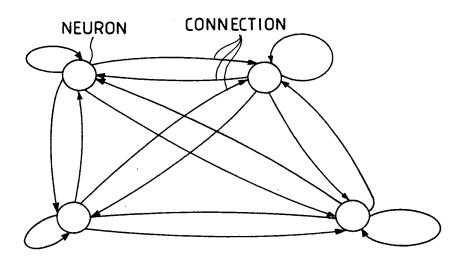
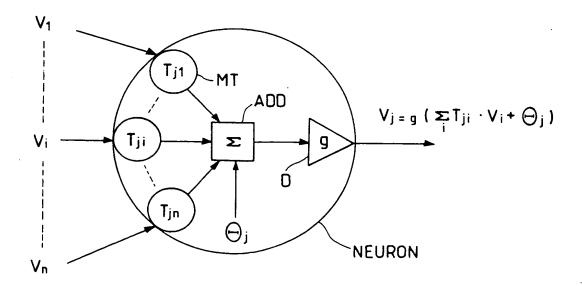


FIG. 3(b)



## FIG. 4 PRIOR ART

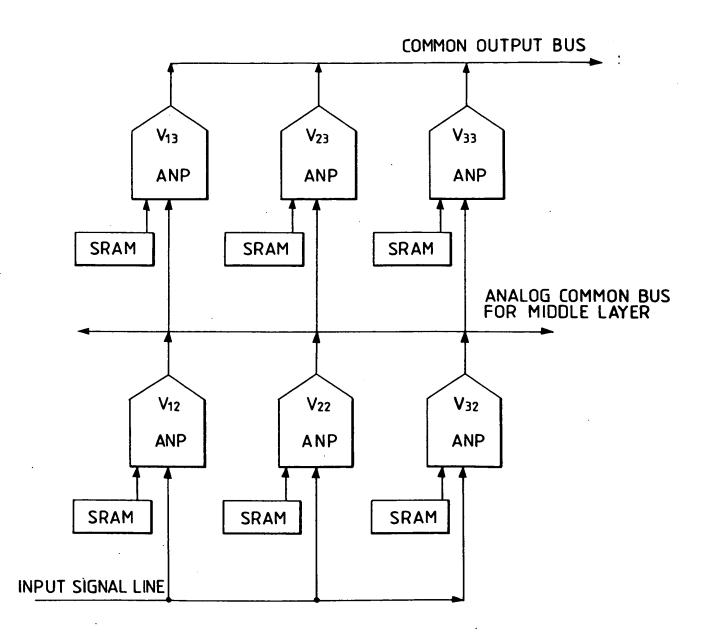


FIG. 5(a)

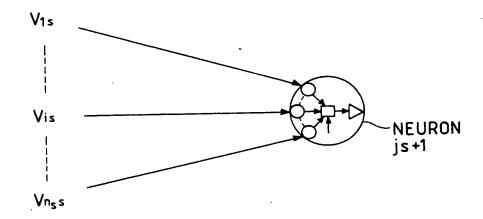


FIG. 5(b)

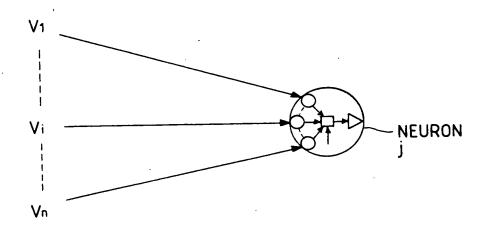


FIG. 6(a)

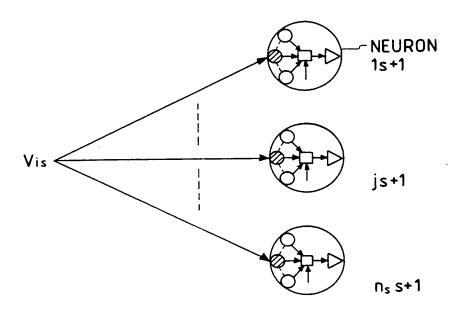


FIG. 6(b)

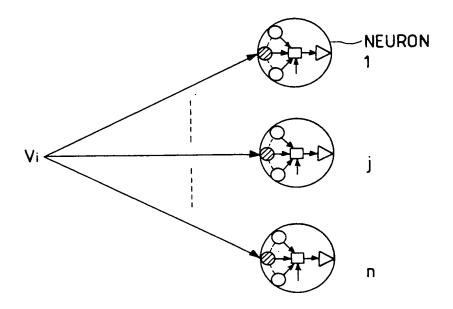




FIG. 7(a)

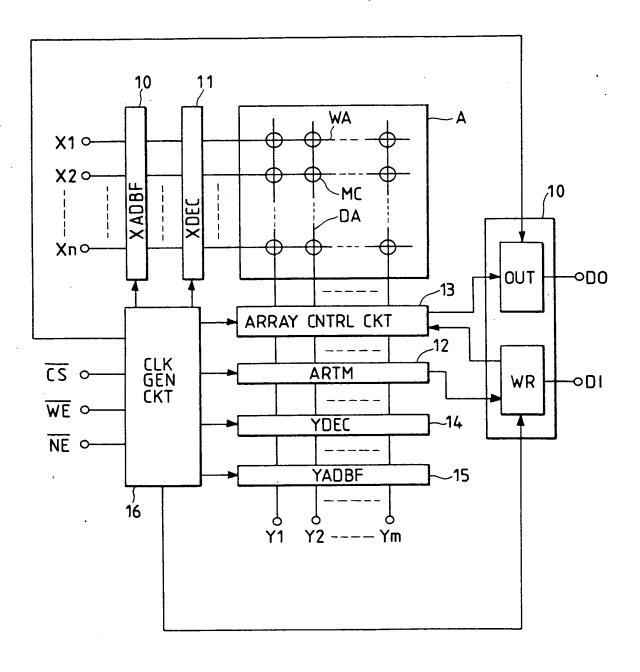


FIG. 7(b)

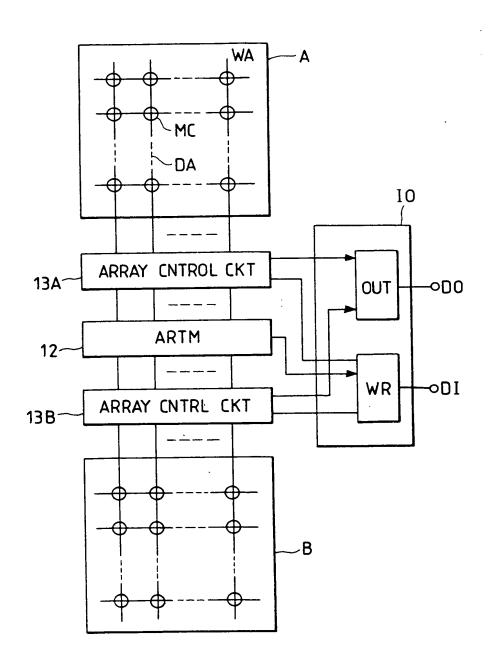


FIG. 8(a)

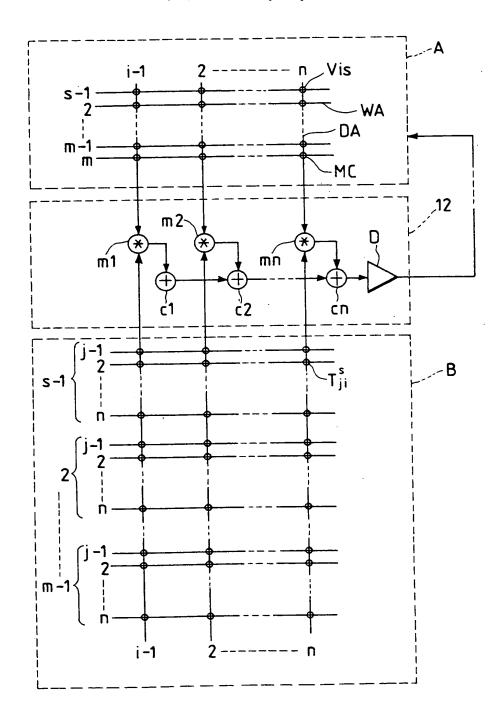


FIG. 8(b)

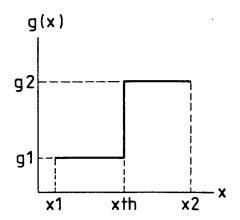
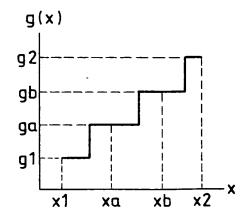


FIG. 8(c)



## FIG. 8(d)

SELECTE WORD	ED LINE	WRITE ADDRESS				
ARRAY A	ARRAY B	ARRAY A (s,i)				
1	(1,1)	(2,1)				
1	(1,2)	(2, 2)				
1	1					
1	(1,n)	(2,n)				
2	(2,1)	(3,1)				
2	(2,2)	(3,2)				
2	(2,n)	(3,n)				
m -1	( m -1,1 )	(m,1)				
m -1	(m-1,2)	(m,2)				
m - 1	(m-1, m)	( m , n)				

FIG. 9(a)

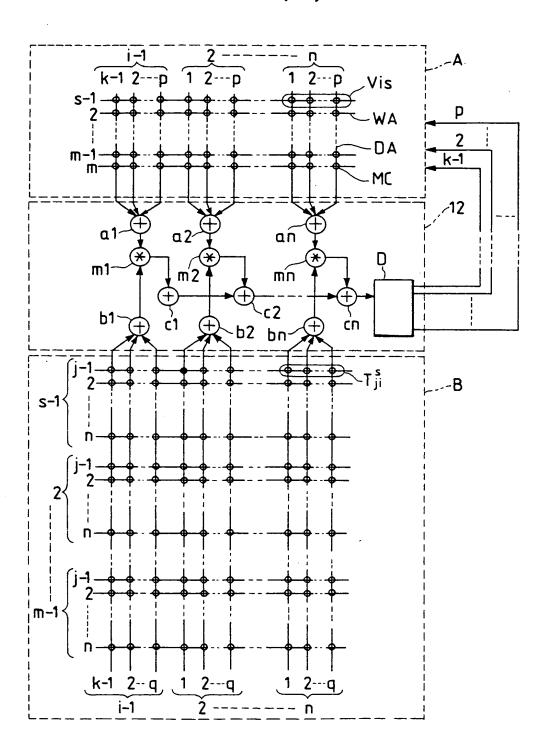


FIG. 9(b)

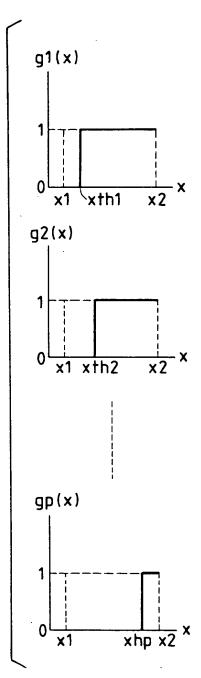


FIG. 9(c)

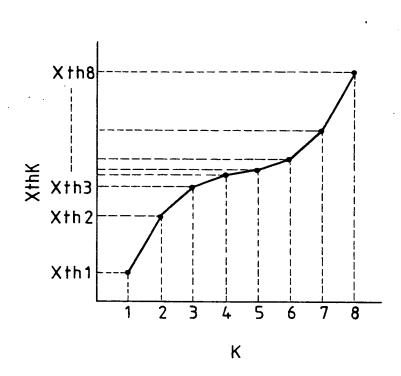


FIG. 10(a)

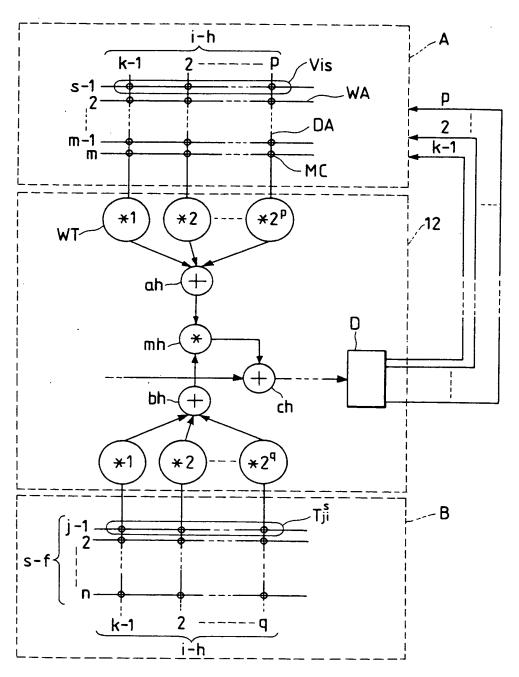
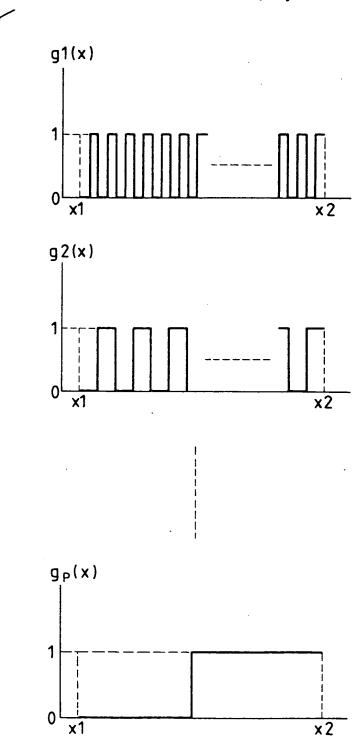
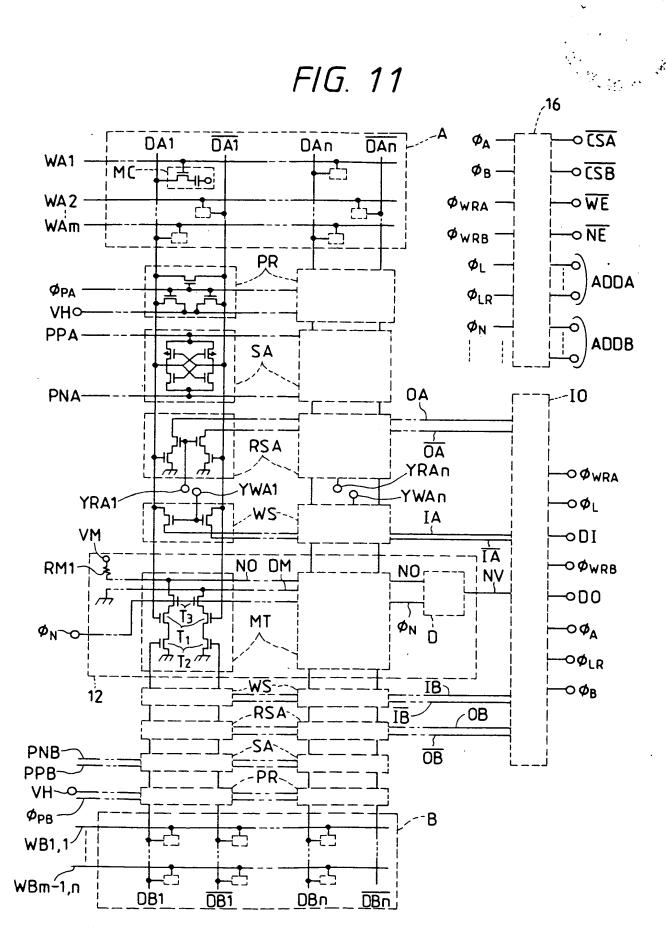


FIG. 10(b)



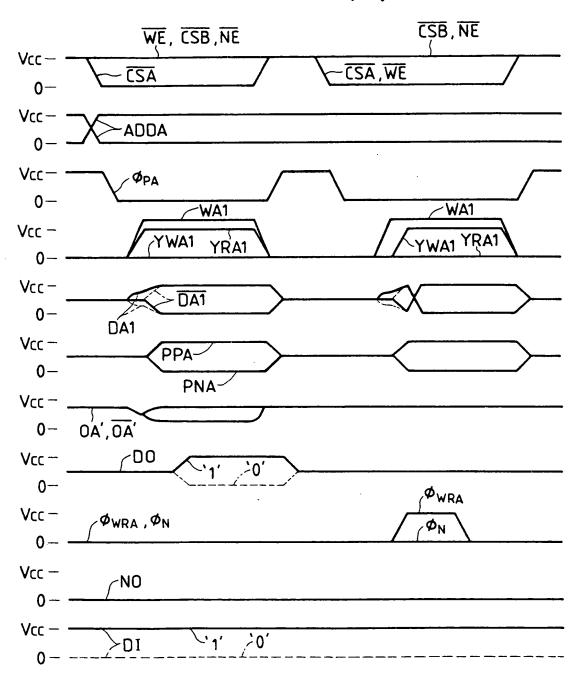
- }



## FIG. 12

MODE		MEMORY	ARITHMETIC MODE			
SIGNAL	AR	AW	BR	ВW	NR	NW
CSA	L	L	Н	Н	L	L
CSB	Н	н н		L	L	Н
WE	Н	L	Н	L	Н	L
ΝĒ	H	Н	Н	Н	L	L
ADDRESS	ADDA	ADDA	ADDB	ADDB	ADDA ADDB	ADDA

FIG. 13(a)



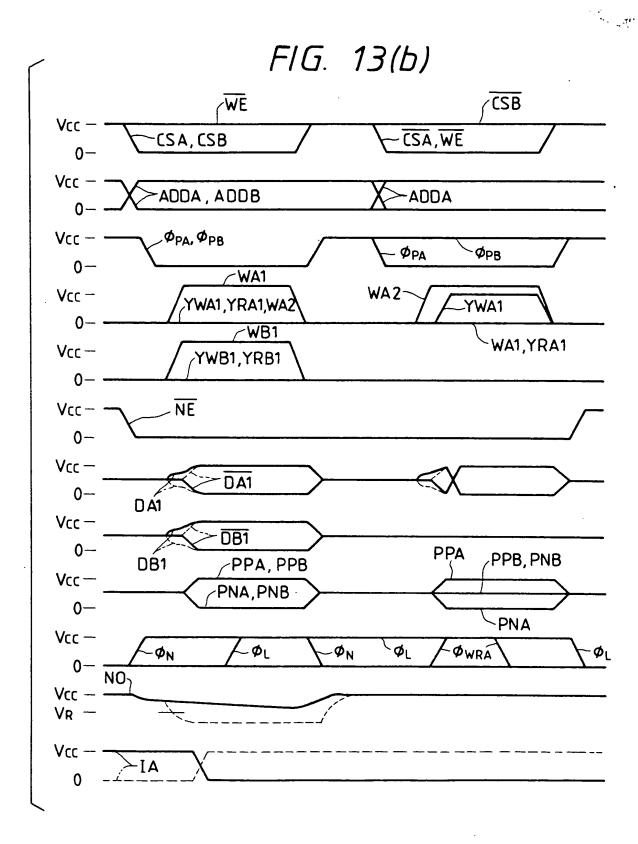


FIG. 14(a)

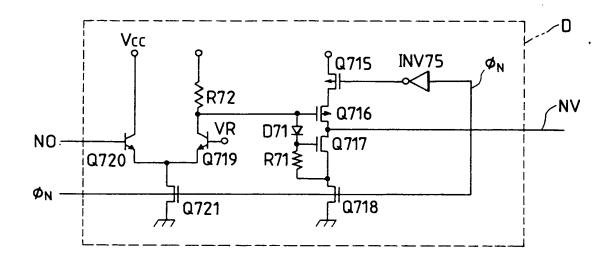


FIG. 14(b)

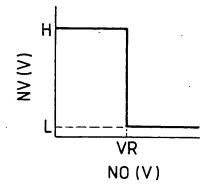


FIG. 14(c)

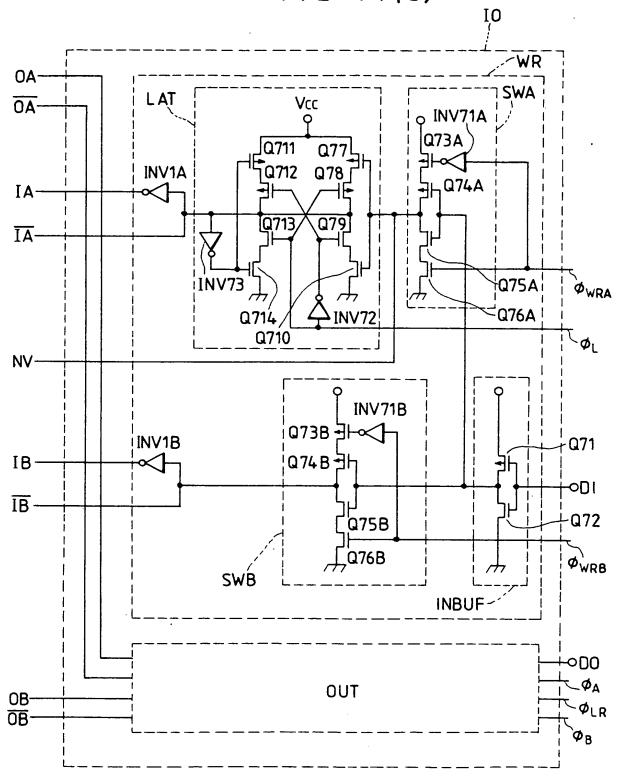


FIG. 14(d)

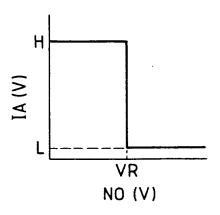


FIG. 14(e)

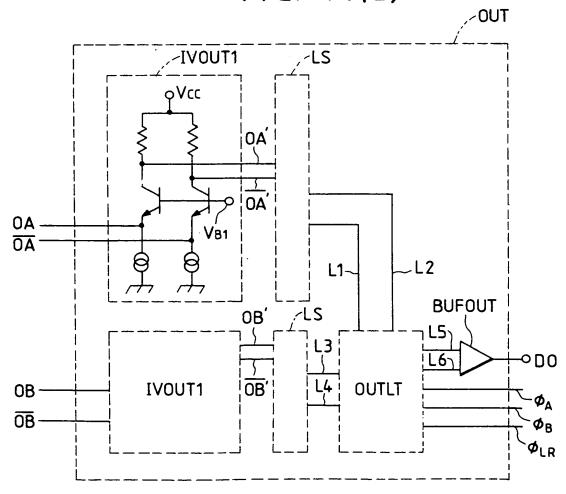


FIG. 14(f)

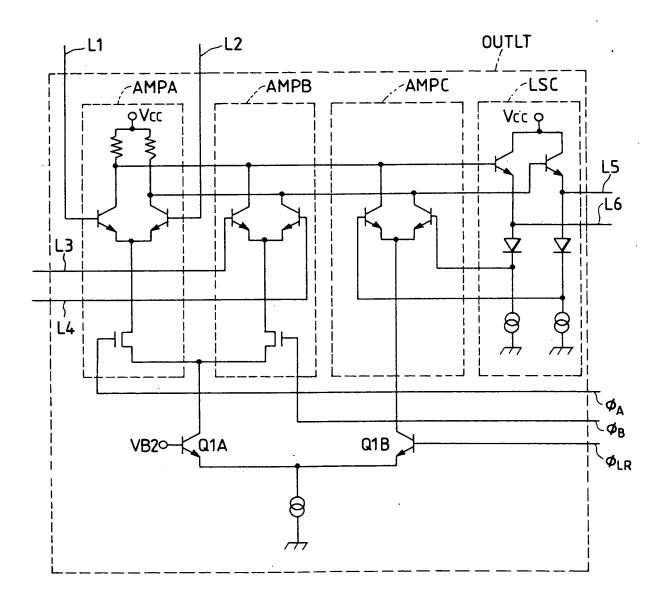


FIG. 15

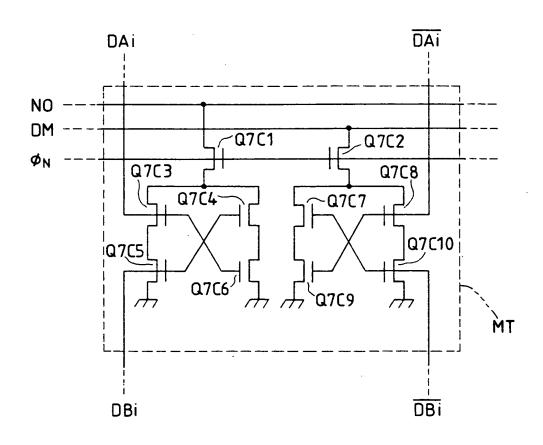


FIG. 16(a)

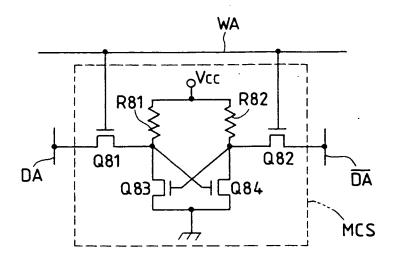


FIG. 16(b)

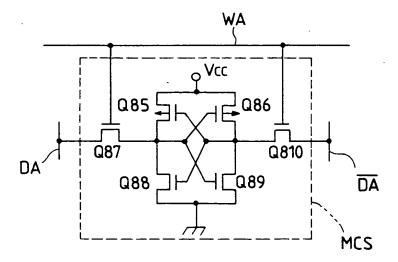


FIG. 16(c) 16 ~<del>CSA</del>  $\phi_{\mathsf{A}}$ ~ <u>CSB</u>  $\phi_{\mathsf{B}}$ Vcc Q ĻD  $\phi_{WRA}$ → WE → NE  $\phi_{WRB}$ WA1 DA1 DAn DA<sub>1</sub> ΦL DĀn ADDA WAm  $\phi_{\mathsf{LR}}$ MCs- $\phi_{N}$ ADDB QΑ ⁄I0 RŞA OA YRAn Ø<sub>WRA</sub> YWA1 YRA1 YWAn ws ΙA VΜ ODI ĪĀ NO DM ~ Ø<sub>WRB</sub> RM1 ŊV **⊙**D0 Ø<sub>N</sub>-ΜŢ \_ ØA 112 \_Ø<sub>LR</sub> ĮΒ ∠ø<sub>B</sub> WS-WS-ĪB^ OB RSA-RSA-ÓВ DB1 DBn DB1 WB1,1 **DB**n LD WBm-1,n MCs

## FIG. 17(a)

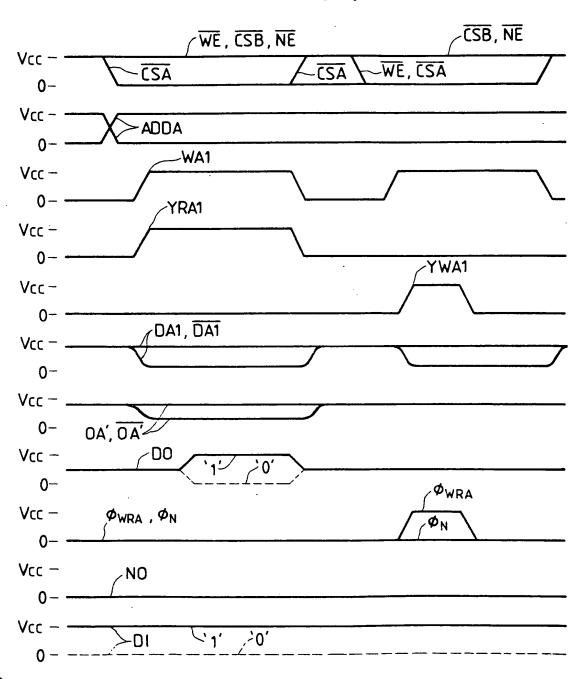
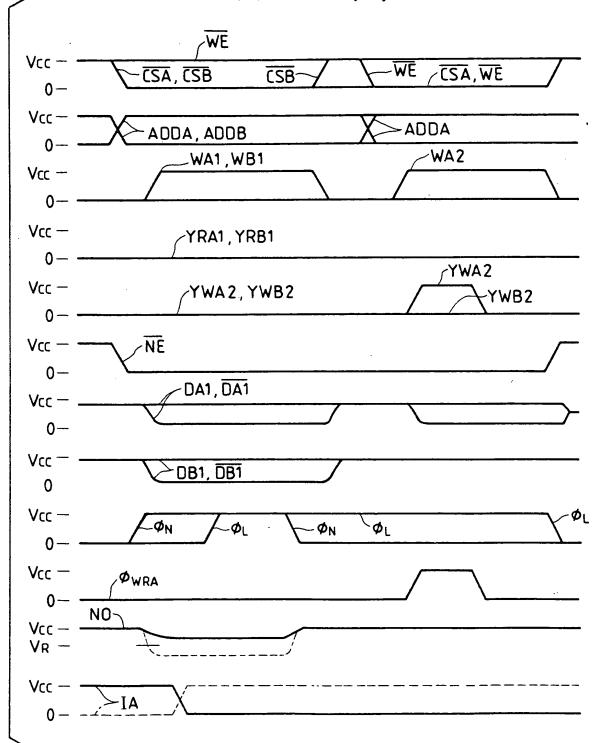


FIG. 17(b)



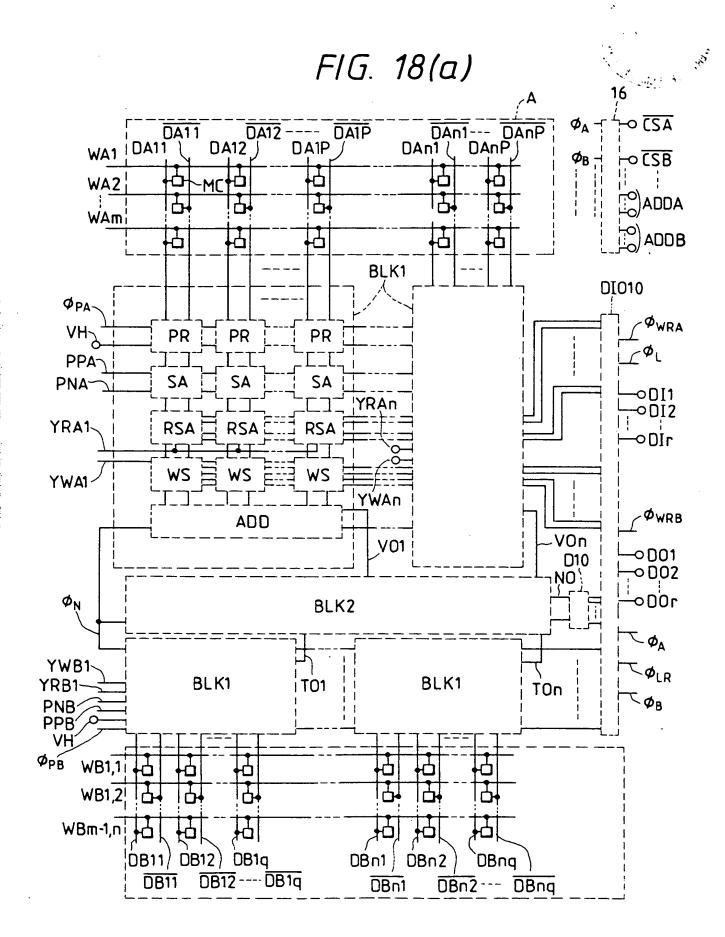


FIG. 18(b)

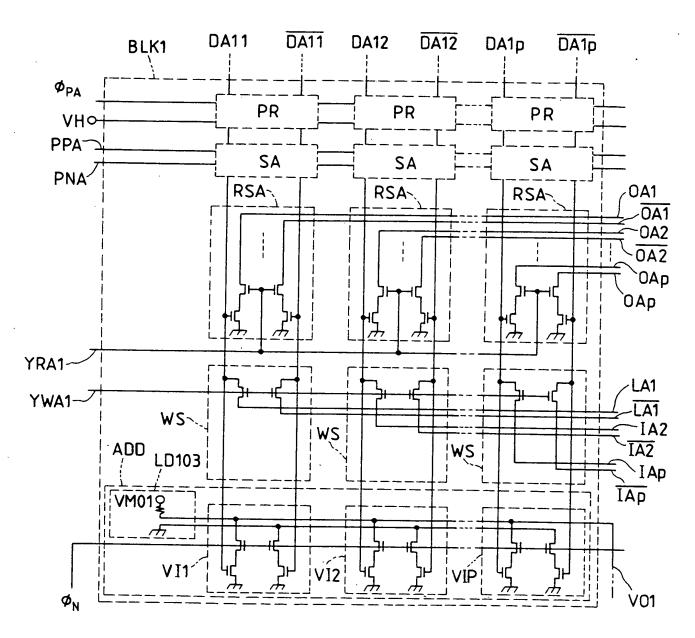


FIG. 18(c)

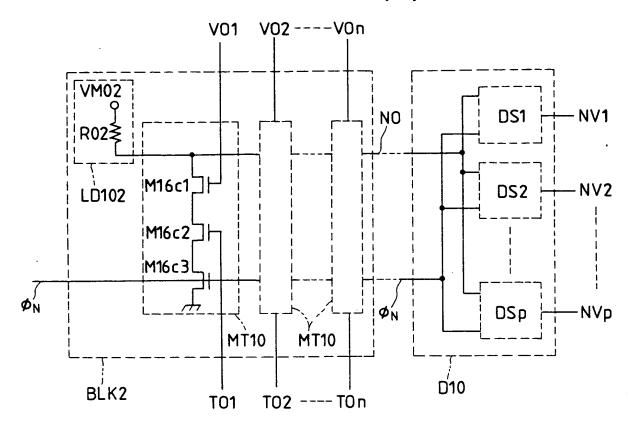


FIG. 18(d)

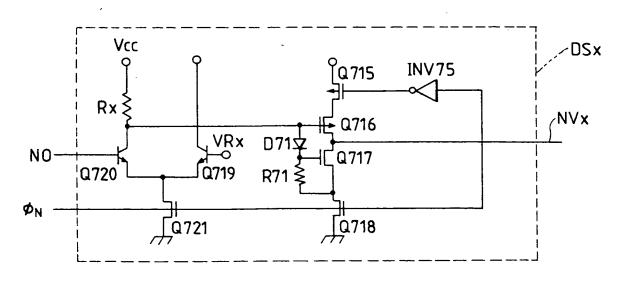


FIG. 18(e)

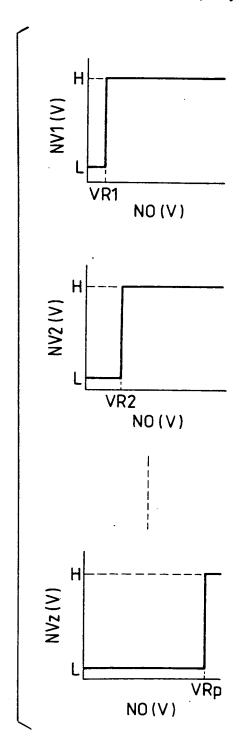


FIG. 19(a)

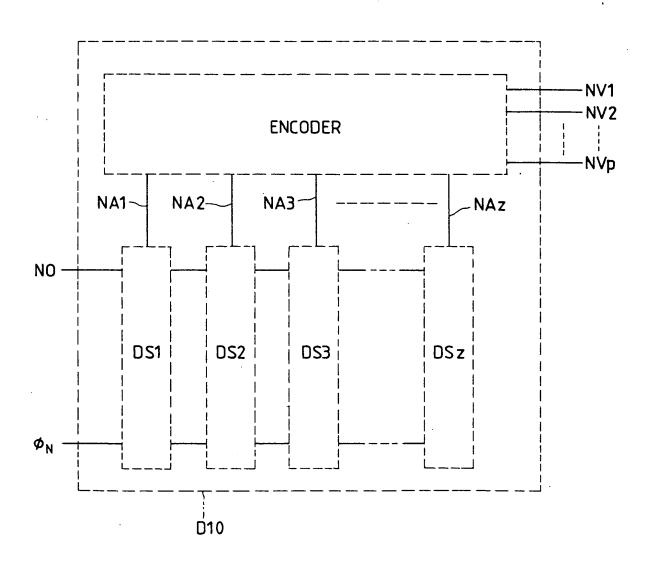


FIG. 19(b)

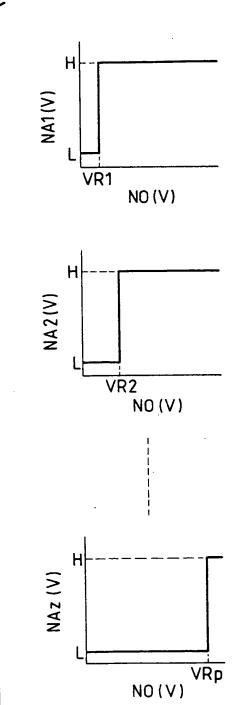


FIG. 19(c)

ENCODER INPUT				ENCODER OUTPUT						
NA1	NA2	NA3	NA4	 NAz	NV1	NV2	NV3	NV4		NVp
L	L	L	L	 L	L	L	L	L		L
Н	L	L	L	 L	Н	L	L	L		L
Н	Н	L	L	 L	L	Н	L	L		L
Н	Н	Н	L	 L	Н	Н	L	L		Ļ
Н	Н	Н	Н	 L	L	L	H	L		L
-	:					i		!		
Н	Ξ	Н	Н	 Н	Н	Н	Н	Н		Н

FIG. 19(d)

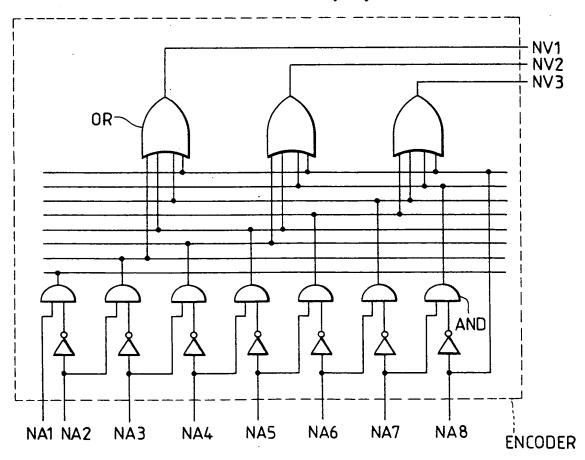


FIG. 20(a)

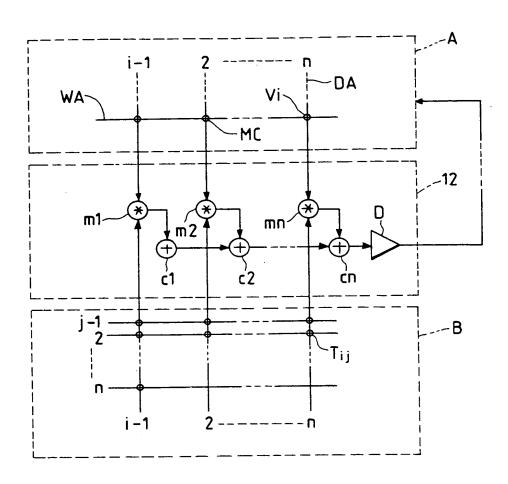


FIG. 20(b)

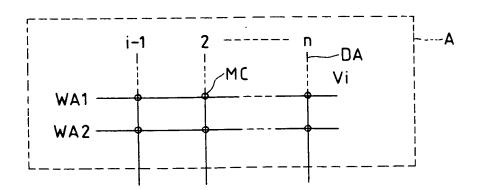


FIG. 21(a)

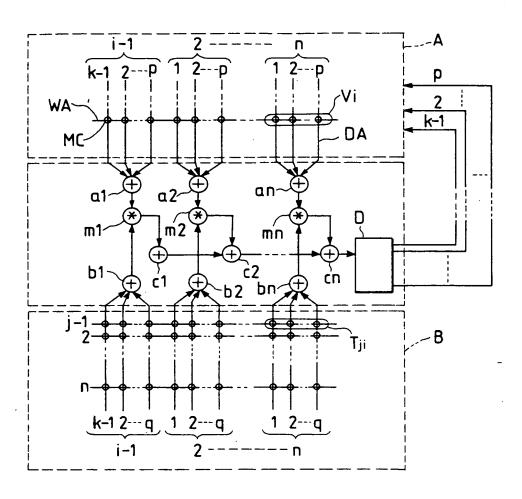


FIG. 21(b)

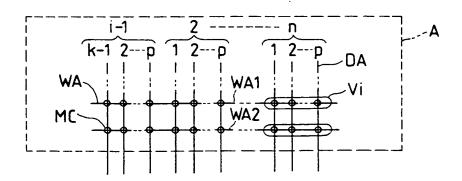


FIG. 22 MEMORY CELL ARY 12 - a1 \_an m1 mn ABS SIGN-SWįn SŲ1 NO EORn EOR1 -NO' cn-- b1 bn-MEMORY CELL ARY В

FIG. 23

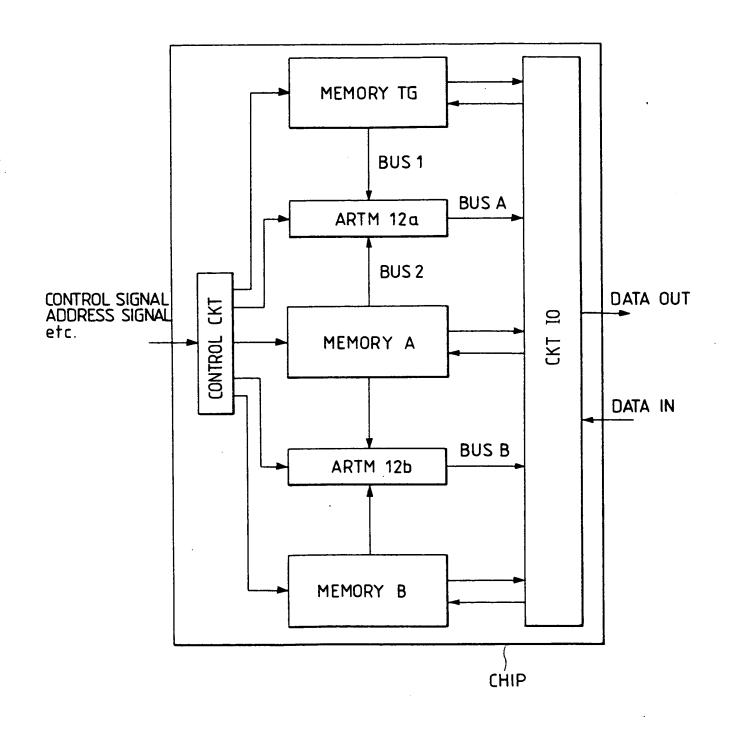


FIG. 24

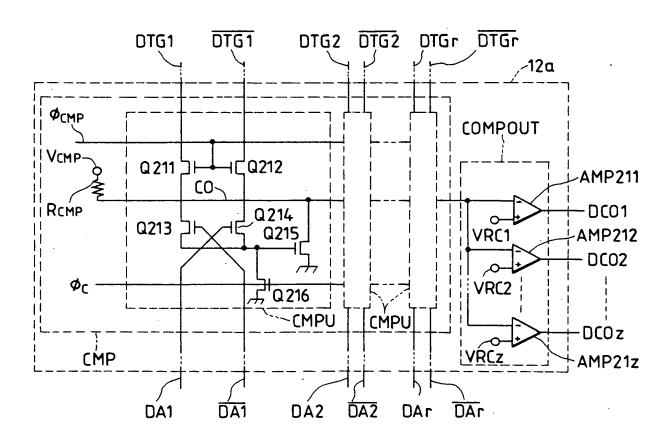


FIG. 25

